

FULLY DIFFERENTIAL, HIGH Q, ON-CHIP, IMPEDANCE MATCHING SECTION

5

ABSTRACT OF THE DISCLOSURE

An inductor circuit is disclosed. The inductor circuit includes a first in-silicon inductor and a second in-silicon inductor each having multiple turns. A portion of the multiple turns of the second in-silicon inductor is formed between turns of the first in-silicon inductor. The first and second in-silicon inductors are configured such that a
10 differential current flowing through the first in-silicon inductor and the second in-silicon inductor flows in a same direction in corresponding turns of inductors.